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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lowrey et al.

Serial Number: 09/891,157

Filing Date: June 26, 2001

Examiner/Group Art Unit: Pham, Hoai V./2814

Title: METHOD FOR MAKING PROGRAMMABLE RESISTANCE MEMORY  
ELEMENT

## CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8

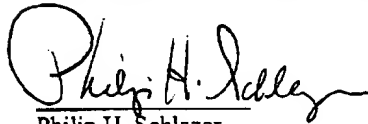
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed in this correspondence is:

1. Response to Office Action dated April 25, 2003.
2. Request for Two-Month Extension of Time

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office,

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